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TITLE: EFFICIENT IMPLEMENTATION OF A DECISION DIRECTED PHASE LOCKED LOOP (DD-PLL)
. FOR USE WITH A SHORT BLOCK CODE IN DIGITAL COMMUNICATION SYSTEMS
INVENTOR(S): LINSKY, ET AL.
SERIAL NO.:09/929,464
REPLACEMENT SHEET 99 50 64 **Block Decoder** Rectangular Converter Polar to 9 62 **68** Loop Filter Magnitude FIG. 8 56 Matched Filter/ Sampler Polar Converter Rectangular to Q Down Converter Synchronous Demodulator 54 -